IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:

Maciuca, Ceglio

Title:

Scalable Wafer Inspection

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2003.07.28

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

Sir:

In compliance with 37 C.F.R. §§ 1.97 and 1.98, applicants provide this Information Disclosure Statement and accompanying form PTO-1449, listing the citations of references to be considered in the above referenced case. By this submission applicants are not admitting the materiality of these citations; they are merely submitted to ensure full compliance with 37 C.F.R. § 1.56.

Sincerely,

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FORM PTO-1449	Atty Docket:	58158.US	Serial #:
INFORMATION DISCLOSURE CITATIONS	Applicant:	Maciuca, Ceglio	
IN AN APPLICATION	Filing Date:	2003.07.28	Group:

U.S. PATENT DOCUMENTS

Examiner Initial	Cite#	Document Number	Date	Name	Class	Sub-Class	Filing Date
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Examiner	Cite#	Author, title, date, pertinent pages, etc.		
Initial		, , , , , , , , , , , , , , , , , , , ,	·	

Examiner	Date Considered:			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in				
conformance and not considered. Include copy of this form with next communication to Applicant.				

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